

Practitioner's Docket No. MI22-1824**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Taylor, Theodore M.

Application No.: 10/056,179

Group No.: 2814

Filed: 01/22/02

Examiner: Howard Weiss

For: Floating Gate Transistors and Methods of Forming Floating Gate Transistors

**Assistant Commissioner for Patents
Washington, D.C. 20231****CERTIFICATION OF FACSIMILE TRANSMISSION**


I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9317 on the date shown below:

Response to May 13, 2002 Office Action

(previously submitted Supplemental Information Disclosure Statements, not yet initialed: Information Disclosure Statement and 1449 dated January 22, 2002 - **Please have the Examiner initial the 1449 and return to this office via facsimile: 509-838-3424, Attn. Robin**)

5/24/02
Date

Robin Saldivia


Signature

Total Pages 18

(Certification of Facsimile Transmission--page 1 of 1)